



Serving the San Francisco, Oakland-East Bay, and Santa Clara Valley Sections of the IEEE For an online version of this Council announcement, with active links, visit www.e-grid.net/announcements/grid0507a.html

below: Consultant, Instructor Positions available

DSP, Networking Classes at SJSU

MEMS/NEMS Packaging, Manufacturing Confs

BioInformatics, Magnetic Recording Confs

Subscribe

Download the new July GRID.pdf (850 kB) -- details on upcoming Chapter meetings, classes, conferences on a wide variety of topics. Select one of the Chapter events to learn about new developments and to network with fellow professionals -- choose a meeting, course or conference to master new skills. See our QuickRef Calendar for a more detailed listing of Chapter meetings.

Chapter Meetings for June, July:

SCV-CE - 6/28 | Embedding Small-Form-Factor Hard Disk Drives with CE-ATA - 3 speakers on new disk drive interface standard for consumer electronics applications ... [more]

SCV-EMS - 6/29 | Management Tools for Mining Knowledge Retention - two talks on managing knowledge web services, and on knowledge-mining tools ... [more]

SCV-IM - 7/6 | Beyond Standard Finite Elements - techniques to make FEA programs do what you never thought they could ... [more]

SCV-EDS - 7/12 | Dielectric Scaling Challenges And Approaches In Floating Gate Non-Volatile Memories - new approaches to meet reliability and performance requirements ... [more]

SCV-LEOS - 7/12 | Making a Many-Colored Processing Engine: Signal Processing with Optical Filters - ... [more]

OEB-Comm - 7/19 | IEEE 802.16 Wireless Metropolitan Area Networks - the interface standard for fixed wireless networks for broadband delivery ... [more]

SCV-Nano - 7/19 | Molecular-Scale Technologies for Electronics and Life Sciences - new ways to apply the techniques of electronics technology to the field of life sciences ... [more]

SCV-CPMT - 7/21 | Reliability of Lead-free Solder Joints: Intermetallic Reactions - solder reaction, spalling, and Kirkendall void formation ... [more]

The Council Marketplace

Do you require someone with special skills -- DSP design, RF, reliability, project planning, chip design, communications, Windows drivers, patent consultations? ... Reference designs, PCB layout, thermal measurements, VHDL, ANSYS, Verilog? See our Council Marketplace page. Thank you for supporting our advertisers.

Note to All IEEE Members

SCV PACE (Professional Activities Committee for Engineers) has the opportunity to put together a PE (Professional Engineer) Review class this summer for the Electrical Engineering discipline, in preparation for the October PE exam. The class would start during July and continue into October. There may also be an opportunity to put on an FE (Fundamentals of Engineering) review class (usually the first step toward a PE) if we have enough interest.

Please e-mail John Westmoreland at PE\_FE\_Review@WestmorelandEngineering.com if you are interested in:

-- Taking the course - if so, which one (PE review, FE review, or maybe both?)

-- Interested in volunteering - if so:

- a) As an instructor - please specify subject area
b) As a volunteer

Consultancy Partner Wanted

An electrical and mechanical engineering firm in Oregon with 35 years of experience is looking for an interested party to take over the company or join with the ultimate aim of taking ownership. Consulting is in the utilities, energy, forest, power and transmission industries in the Pacific Northwest and Alaska.

For more details: www.e-grid.net/docs/0507-consult.pdf

Two college-level teaching positions available

Santa Clara University has an opening for a lecturer in EE (with emphasis in communications), and Cogswell College has an opening for an endowed chair in electrical engineering.

For more details: www.e-grid.net/docs/0507-positions.pdf

Professional Skills Courses in the SF Bay Area

Choose from an array of skills-development classes, from Effective Training Associates and the Engineering Management and CPMT Chapters

Finance for Non-Finance Professionals [more]

- July 21 at Exar Corp, Fremont

This half-day course provides non-finance professionals with an understanding of basic financial terms and concepts to enable them to connect their activities and work products with the company's bottom line. The class focuses in three basic areas:

- The Income Statement and Balance Sheet
- Investment financials
- Trends in a company's quarterly report

See the website for more details

Other summer classes:

Collaborative Negotiating for Engineers [more]

- July 12 at EMC Software, Pleasanton

Getting Things Done Across Organizational Borders [more]

- July 21 at BEA Systems, San Jose

Managing Time & Multiple Priorities [more]

- July 22 at Sybase, Dublin

SJSU Department of Electrical Engineering:

August Short Courses

On the SJSU campus (easy parking)

Two classes on August 9-12:

Digital Signal Processing -- 4-day class with labs:

DSP System Design and Implementation [more]

Instructor: Professor Avtar Singh, EE Dept, SJSU

Analog & RF -- 4-day class with labs:

Wireless Transmitters [more]

Instructor: Professor Sotoudeh Hamedi-Hagh, EE Dept, SJSU

Three classes on August 16-19:

Digital System Design -- 4-day class with labs:

Embedded Systems and Embedded FPGAs [more]

Instructor: Professor Tri Caoheu, EE Dept, SJSU

Digital Signal Processing -- 4-day class with labs:

FPGA DSP System Design [more]

Instructor: Professor Chang Choo, EE Dept, SJSU

Networking Engineering -- 4-day class with labs:

High-Speed Data Networks [more]

Instructor: Professor Nader Mir, EE Dept, SJSU

For full class outlines, prerequisites, fees, and lecturer biographies, see the Course Flyer:

www.e-grid.net/docs/sjsu0508.pdf

Circulate within your department.

Need consulting or outsourcing services?

Check out the GRID Marketplace.

ASME Pacific Rim Technical Conference and Exhibition

Integration and Packaging of MEMS, NEMS, and Electronic Systems [more]

- San Francisco Westin St. Francis Hotel

- July 17-22 (Tutorials July 17, sessions/exhibits July 18-22)

Being held for the first time in the SF Bay Area, the InterPACK'05

Conference is an international forum for exchange of state-of-the-art knowledge in research, development, manufacturing, and application in the integration and packaging of MEMS, NEMS, and electronic systems. It covers the disciplines of microelectronics, photonics, microwave, MEMS and NEMS. Emerging

July 11-12: IEEE Int'l Electronics Manufacturing Technology (IEMT)

Symposium. SF Moscone Convention Center - in conjunction with SEMICON/West. [more]

The IEEE forum for research, development, and applications of manufacturing technology for components, assemblies, and systems, with 9 invited talks by industry experts on "Manufacturing at the Wafer Level." Only \$295 for registration.

Complimentary admission to SEMICON/West Exhibits, July 12-14 at Moscone Center - includes keynote talks. See preregistration form (Page 8) for details.

IEMT Professional Development Courses [more]

knowledge, research results, new developments, and novel thermal, mechanical, electrical, and materials packaging product concepts are being presented.

Download the [one-page flyer](#)

Review the program at [www.asme.org/events/](http://www.asme.org/events/)

... held concurrently with the

**ASME Summer Heat Transfer Conference** [\[more\]](#)

A forum for the exchange of state-of-the-art knowledge in research and application of heat, mass and momentum transfer in all fields of mechanical engineering, chemical engineering, nuclear engineering and related technologies.

Visit the website: [www.asmeconferences.org/HT05/](http://www.asmeconferences.org/HT05/)

- Half-day courses
- All on Monday, July 11, 2005
- At SF Marriott Hotel

**Introduction to Nanotechnology: Tools, Processes and Applications** Florin Ciontu and Bernard

Courtois, TIMA Laboratories, Grenoble, France

**Introduction to Flip Chip Technology: A User's Guide** Dr. Dev Gupta, Chief Technical Officer, APSTL, Scottsdale, AZ

**Design, Materials, Processes, and Reliability of Lead-Free Packaging and Assembly** Dr. John Lau, Lead-Free Technical Program Manager, Agilent Technologies, Santa Clara

**Advanced Packaging Technology Solutions for Today's Leading-Edge Microelectronics** Ray Fillion, manager of Advanced Pkgng Programs, GE Global Research, New York

**Failure Mode Analysis of Flip Chip in Package and Board Assemblies** Dr. Daniel Baldwin, Georgia Institute of Technology

August 8-11 on the Stanford University campus (plenty of parking):

IEEE **Computational Systems Bioinformatics Conference** [\[more\]](#)

Come to **CSB'05** for a broad spectrum of peer-reviewed papers covering the breadth and depth of the dynamically evolving field of bioinformatics. Our keynote speakers, paper and poster presentations, tutorials and social events have all been designed to cater to bioinformatics' eclectic mix of disciplines. The low registration fees make it possible for everyone to attend - it's especially easy for "locals" from the Bay Area.

**What is "Bioinformatics"?**

**Bioinformatics** - scientific and engineering disciplines bringing new biological discoveries to fields as varied as human health, agriculture, the environment, energy and biotechnology. Find out more at **CSB'05**, where this year's theme is "Connectivity Matters."

**Tutorials on Monday:**

Demonstration Projects in Clinical Informatics - Novel Visualization and Analysis Methods in BioImaging - Computational Methods in MS-based Proteomics - Introduction to the Semantic Web for Bioinformatics - Pattern Discovery in Sequences and Structures - The Biological Data Deluge through Phylogenetics - Statistical Approaches to Analyzing Biological Networks - and more

See the one-page flyer in this month's [GRID.pdf](#), or visit [conferences.computer.org/bioinformatics](http://conferences.computer.org/bioinformatics).

IEEE Magnetics Society

**16th Annual Magnetic Recording Conference: "Heads and Systems"** [\[more\]](#)

- August 15-17
- Held at Stanford University in Palo Alto
- Early-bird discount through July 18th
- Join leading experts in the magnetic recording field

**Main topics:** Read heads, Write heads, Perpendicular recording heads and systems, Recording systems, Advanced coding/detection and Reliability/Mechanics.

The oral sessions will be held at the Hewlett Teaching Center Auditorium, and Stone Pine Plaza will be used for Posters and Bierstube. On-campus parking at no cost.

Download the [one-page flyer](#)

Get complete information at [tmrc.nanointernational.org](http://tmrc.nanointernational.org)

In today's world, increasing competition has made professional development a requirement -- not an option.

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Technologists who are not members of IEEE are **encouraged to subscribe, at no cost, to this bi-monthly e-GRID Newsletter**, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails. This is a controlled list (ie, no spam) -- [get more information](#). You provide your own subscribe/unsubscribe services and can join and leave this IEEE DList when desired. To update the list, send an email message to:

[listserv@listserv.ieee.org](mailto:listserv@listserv.ieee.org)

...with the following choices in the BODY (not Subject) of the message:

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Maintain your current DList records at our IEEE LISTSERV website: <http://listserv.ieee.org/>

You may need to "whitelist" our email addresses in your spam filter to allow our Council news to reach you; I suggest whitelisting my email address ([editor@e-grid.net](mailto:editor@e-grid.net)), plus that of the DLIST: [e-GRID@listserv.ieee.org](mailto:e-GRID@listserv.ieee.org).

Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See <http://www.e-grid.net/>

Correspondence to [Paul Wesling](#), Council Communications Director