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below: Wescon no-cost events and Exhibits in Santa Clara

RFID, uWave ICs, EMC, Control Systems classes

Effective Email, SoP, Communications classes

Quality Design Symp., MRS Spring Meeting

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Download the March GRID.pdf (750 kB) -- details on upcoming Chapter meetings on a wide variety of topics. Select one of the Chapter events to learn about new developments and to network with fellow professionals -- choose a meeting, course or conference to master new skills. Check for updated information during the month at our GRID website: www.e-grid.net/

Chapter Meetings for March, April:

SCV-CPMT - 3/16 | New, Ultrahigh Thermal Conductivity Materials - improved thermal performance and reliability, simplified thermal design, elimination of fans and heat pipes, weight savings ... [more]

SF-PES - 3/16 | Transbay HVDC Cable Technology - proposed high-voltage direct-current submarine cable for transmission capacity (400 MW) between the Pittsburg and Potrero switchyards ... [more]

SCV-EMB - 3/16 | Gastric Electrical Stimulation - the history of gastric electrical stimulation is discussed, focusing on recent progress in the treatment of obesity ... [more]

ACM - 3/16 | Making Electronic Communications Available to All - hundreds of the world's languages are now in Unicode, while others need to be included ... [more]

OEB-IAS - 3/17 | Considerations in the Application of AFDs - effects of Adjustable Frequency Drives on the electrical power distribution system ... [more]

SCV-SSC - 3/17 | The Road to 60 GHz Wireless CMOS - a 60 GHz multi-antenna phased array for low cost robust high data rate system in a compact package ... [more]

OEB-Comm - 3/17 | Trends and Innovation in RF Identification - RFID, sensor technologies in the tags themselves, and architectural requirements ... [more] - How will they be made and tested? [more]

SCV-CE - 3/22 | Panel Discussion: Video on Demand (VOD) - home networking and service provider solutions for choice and flexibility in Internet based VOD ... [more]

SCV-CNSV - 3/22 | Role of Computer Tomography in Molecular Imaging - this technique will become the future of medical imaging, especially in gene therapy ... [more]

SCV-Nano - 3/22 | Stanford Nanofabrication Facility - a state-of-the-art, shared-equipment, open-use resource to serve researchers ... [more]

SCV-ReI - 3/23 | How Cosmic Rays Cause Computer Downtime - more users and designers must now contend with soft error possibilities in their designs ... [more]

SCV-Ed - 3/23 | Shaping the Future of Engineering Education in Silicon Valley - current challenges and possibilities for shaping the future of engineering education in the Valley ... [more]

SCV-CPMT - 3/24 | Thinning and Dicing Ultra-thin Wafers - advances with increased functionality and capacity ... [more]

SCV-ED + Stanford Students - 3/24 | Challenges in Building a Strategic Information Infrastructure - the illusion of a virtual database containing all data of interest to users ... [more]

SCV-Mag - 3/29 | Origin of Tunneling Spin Polarization - spin polarization and magnetization of current tunneling from a ferromagnetic material through a tunnel barrier ... [more]

SCV-EMS - 3/30 | Career Checkpoints and Introducing Technology to Make Everyday Devices See - check pointing your career, followed by time-of-flight 3D sensor chips for computer imaging, vision, distance measurement ... [more]

SCV-ED - 4/12 | VLSI Devices for Analog and Mixed-Signal Applications - ... [more]

SCV-Comm - 4/13 | Where is the RFID puck Going Next? - Wal-Mart, Target, Albertsons, Best Buy, Department of Defense, casinos, passports: where next? ... [more], How will they be made and tested? [more]

SCV-EMB - 4/20 | Tactile Pressure Imaging: A New Tool For In Vivo Physiological Assessment for Gastroenterology - portable advanced pressure imaging system with high fidelity pressure maps of the gastrointestinal tract ... [more]

SCV-PACE - 4/26 | Resume-writing Clinic - review the structure of a good resume, and bring your own for critique ... [more]

SCV-CNSV - 4/26 | Who Owns Your Brain? - a consultant's guide to patents and intellectual property ... [more]

SCV-CS - 5/7 | Sensor Networks - The New Environment - Saturday seminar, at Stanford ... [more]

The Council Marketplace

Do you require someone with special skills -- RF, reliability, project planning, communications, design, patent consultations? ... PCB layout, thermal measurements, SCSI, SANs? See our Council Marketplace page. Thank you for supporting our advertisers.

Try out a new publication at half-price? This is the time of year when any IEEE member can add one or more publications for the balance of 2005 at the half-price rate. It's a great chance to try out one of the Societies you've thought about joining, and sampling its services and magazines/journals. You could check out the Nanotechnology, Nanobioscience, or Advanced Packaging journals (or dozens of others) for several issues, or get Computer or Microwave magazine. To update the features of your membership, call 1-800-678-IEEE, or do it online.

April 12-14, Santa Clara Convention Center

Theme: D2M -- from Design to Manufacture

Wescon Previews [more]

Welcome to the exciting world of Wescon, and its new location in the Santa Clara Valley. Plan to spend a full day at the exhibits and programs. Most of the program is at no cost, and there's plenty of parking.

Included with complimentary admission: talks by eight Presidents/CEO's, 2 VP's of Engineering, two CTO's from IBM Research, and much more.

Design Integration Showcase

IEEE Wescon and the Silicon Integration Initiative (SI²) will have a special pavilion showcasing EDA IC design-for-manufacturability technology within the Design Automation Theater at the Convention Center.

Tutorials and Panels (no fee): - Innovative Approaches to Tackling the Challenges of D2M - DFM: The Physics - Yield: The Basics - Lithography Challenge - Design Optimization Yield - Test to Yield Prediction - Design for Yield: Mixed Signal Analog - Library Challenge - Meeting the Challenges of Complex Systems Design

Test & Measurement Track

Tutorials and Panels (no fee): - Advances in Signal Integrity Testing - Testing and Troubleshooting Serial Data Signals - Getting More Out of Your Digital Oscilloscope - Productivity Features in Windows Oscilloscopes - Debugging Elusive Digital System Problems Using Logic Analyzer Triggering - Faster Ways to Characterize Transient RF Signals - Test Data Management System for Product Development Process - High Speed Probing and Signal Fidelity

Download the 5-page Wescon Preview (280kB PDF)

Electromagnetic Compatibility Chapter, SCV

Electromagnetic Compatibility Courses

- Four Half-Day Tutorials (choose one, or all four)

- In conjunction with IEEE Wescon'05

Fundamentals of Electromagnetic Compatibility

Fundamental Concepts of Signal Integrity for PC Boards

Microwave Theory & Techniques Chapter, SCV

Designs for the High-Speed, Broadband Information Age [more]

- 8:00 AM - Noon, Wednesday April 13, 2005

- In conjunction with IEEE Wescon'05

- Four Presentations:

100GHz CMOS Circuits for the Broadband Information Age

SiGe and RFCMOS for the High Speed Information Age

InP HBT Design for 100 to 200GHz ICs

SiC and GaN Based Transistor and Circuit Advances

Register by April 9th to get discount rate (\$50 for Members)

Access the Advance Program and registration form

Components, Packaging & Mfg Technology Chapter, SCV

Packaging and Test Technology for RFID [more]

- 1:00 PM - 5:30 PM, Wednesday April 13, 2005

- In conjunction with IEEE Wescon'05

- Five Presentations:

Overview of RFID Technology and Deployment

Packaging Technology for UHF RFID Tags

International RFID Frequencies and Package Outline Standards

Packaging for RFID Antennas, Controllers, Interface Modules

RF/Microwave Packaging at Freescale Semiconductor

Register by April 1st to get discount rate (\$75 for Members)

Full Details and Registration Information

Control Systems Chapter, SCV

Control Systems Technology Seminars [more]

- Wednesday, April 13

- In conjunction with IEEE Wescon'05

Motor Control Fundamentals 8:30 AM - 11:00 AM

Understanding Robotic System Control 11:30 AM - 2:00 PM

Control System Design: A Refresher 2:30 - 5:00 PM

Testing for EMC Compliance - Approaches and Techniques
Fundamentals of Grounding, Shielding for Noise Reduction
Register on the [Wescon website](#)

[More Information and Registration Details](#)

Lead-Free Soldering Implementation Tutorials [\[more\]](#)

- Conversion to Lead-Free, and its Impact on Your Business
- Impact of Lead-free Solder on Board Assembly, and the Successful Lead-Free Implementation Strategy
[Additional details](#) (300kB PDF - see last page)

SCV Chapters, Engineering Management & Components, Packaging and Manufacturing Technology Societies

Professional Skills Development and Management Classes

Enjoy lifetime employability; stay ahead of changing professional demands; sharpen your competitive edge with up-to-date skills. [\[more\]](#)

new! "Writing Effective E-mail Messages

- Date/Time: Tuesday, March 15, 8:30AM-4:30PM
- Location: Synopsis, Sunnyvale

Getting Things Done Across Organizational Borders

- Date/Time: Thursday, March 24, 8:30AM-4:30PM
- Location: Synopsis, Sunnyvale

new! Influential Communication

- Date/Time: Thursday, March 5, 8:30AM-4:30PM
- Location: Exar Corp, Fremont

Full [course descriptions and registration details](#).

Pre-Announcement:

SOP vs SiP, vs SOC: Technology Directions for Systems Implementation --

Emerging and Disruptive Packaging Technologies for the Next Decade [\[more\]](#)

7 Speakers from Georgia Tech's Packaging Research Center

- Date: Wednesday, May 4, 8 AM - 5 PM
- Location: Ramada Inn, Sunnyvale
- Cost: only \$95 for IEEE Members (thru April 25)
- Web: www.cpmt.org/scv/

"Design for Quality in the Era of Uncertainty"

International Symposium on Quality Electronic Design [\[more\]](#)

- Sponsors: EDS - CAS - CPMT - SigDA

- March 21-23, 2005 at the Double Tree Hotel, San Jose

TUTORIALS (all day Monday): - Design of sub-90nm CMOS Circuits and Design Methodologies - Package-Chip Co-Design

Technical Sessions: - Leakage and Dynamic Power Issues - Test Application and Cost Reduction - Performance and Reliability Analysis for Yield Optimization - Functional Verification and Test Generation - DFM for Circuit Design - Analog Test and BIST- Variability Issues in Nanoscale Circuits - Advances in Floor Planning - Robust Design under Parameter Variations (and more)

[Get complete information.](#)

2005 Materials Research Society Spring Meeting & Exhibit [\[more\]](#)

- 28 March - 1 April 2005

- Moscone Convention Center (West), San Francisco

Take BART or Muni to the Powell Street Station

The **MRS Exhibit** features international exhibitors from all sectors of the materials science and engineering communities. Visit the exhibit to learn more about the latest techniques and advances in the swiftly evolving world of materials research directly from the manufacturers, suppliers and developers. **Admission to the exhibit only is free of charge.**

35 Symposia tracks with technical sessions highlight advances in the synthesis, characterization, simulation, and application of materials, from fundamentals to fabrication of devices using electronic and electro-optic materials, nanostructured materials, and biological and hybrid materials.

Review the [Advance Program and registration form](#).

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Technologists who are not members of IEEE are **encouraged to subscribe, at no cost, to this e-GRID Newsletter**, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails. This is a controlled list (ie, no spam) -- [get more information](#). You provide your own subscribe/unsubscribe services and can join and leave this IEEE DList when desired. To update the list, send an email message to:

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Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See <http://www.e-grid.net/>

Correspondence to [Paul Wesling](#), Council Communications Director