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[Test/Failure Analysis, Unicode conferences in Nov.](#)

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Download the **new November GRID.pdf** (1.1 MB) -- details on upcoming Chapter meetings, classes, and conferences on a wide variety of topics. Stay ahead of changing professional demands; sharpen your competitive edge with up-to-date technical skills and through your networking activities. See our **QuickRef Calendar** for a more detailed listing of Chapter meetings.

Chapter Meetings for November:

SCV-LEOS - 11/1 | **Solid State Light Sources** - Addressing the barriers that limit their application to general illumination ... [\[more\]](#)

SCV-IM - 11/2 | **Blackfin: A Combined RISC/Signal Processing Architecture for a New Era** - embedded processor for audio, video, and communications applications ... [\[more\]](#)

SCV-Mag - 11/3 | **Toggle MRAM Performance and Outlook** - magnetoresistive random access memory integrated with silicon-based microelectronics ... [\[more\]](#)

SCV-EDS - 11/7 | **A Survey of Semiconductor Devices** - from the old and sometimes obsolete types to the recent quantum-well devices, organized into groups ... [\[more\]](#)

SCV-EMC - 11/8 | **BPL Emissions Compliance Guidelines** - controversy has surrounded the deployment and testing, requiring means for assessing compliance and mitigating interference ... [\[more\]](#)

SCV-CPMT - 11/9 | **Carbon Nanotubes: Enhancing Conductivity of Conductive Plastics** - better interconnect materials, thermal spreaders, and even sensors and display structures ... [\[more\]](#)

SCV-GOLD - 11/9 | **Challenges and Innovations for Development of SOCs** - for everyone, but especially those in the first decade of their careers ... [\[more\]](#)

SCV-MTT - 11/10 | **On the Use of Artificial Intelligence in Microwave Hardware Design** - use of Artificial Intelligence Optimizer ("AIO") antenna design software for real-time optimal solutions ... [\[more\]](#)

SCV-Nano - 11/15 | **Nanotech: Imagine the Opportunities** - half-day symposium showcasing university graduate research ... [\[more\]](#)

SCV-EMB - 11/16 | **High Frequency Ultrasound Imaging of the Anterior Segment of the Eye** - in-vivo intra-operative visualization of the Schlemm's canal for diagnosis and surgical guidance ... [\[more\]](#)

OEB-IAS - 11/17 | **Arc Flash Studies using Electrical Engineering Software** - a focus on how to perform an arc flash study using electrical engineering software ... [\[more\]](#)

SCV-SSC - 11/17 | **PLL History: A Personal Viewpoint** - from the extremely narrow-band systems of years ago to the wide-band applications of today ... [\[more\]](#)

SCV-CAS - 11/21 | **A Digital Clock and Data Recovery Architecture for Multi-Gigabit/s Binary Links** - a general architecture and a small-signal model ... [\[more\]](#)

SCV-CE - 11/29 | **Distributed HD IPTV and Audio over Powerline** - no new wires and it works throughout the whole house ... [\[more\]](#)

Note to All IEEE Members

The Council Marketplace

Do you require someone with special skills -- DSP design, RF, reliability, test design, project planning, chip design, communications, Windows drivers, patent consultations? ... Reference designs, PCB layout, thermal measurements, VHDL, ANSYS, Verilog? See our **Council Marketplace** page. Thank you for supporting our advertisers.

SID 2006 Call for Papers

Society for Information Display International Symposium, Seminar, & Exhibition [\[more\]](#)

- Paper Summary Deadline: **December 1, 2005**
- Location & Dates: San Francisco, June 11-13, 2006

SID '06 will have 250+ exhibitors and 7,500+ attendees - display scientists, engineers, manufacturers, entrepreneurs, marketers and end-users with original papers on all aspects of research, engineering, application, evaluation, and utilization of displays.

Papers are solicited in the following areas:

- Active-Matrix Devices - Display Applications (consumer, industrial, commercial, military) - Applied Vision/Human Factors - Display Electronics - Display Manufacturing - Display Measurement - Display Systems - Emissive Displays - FEDs and CRTs - Organic Light Emitting Diodes - Projection Displays - Liquid-Crystal and Other Non-Emissive Displays

For the latest information, including complete Call for Papers, download the **one-page GRID announcement** or visit www.sid2006.org

Faculty Positions: UC-Santa Cruz, Computer Engineering Dept Assistant and Associate Professor

- Assistant Professor with research interests in the area of **Assistive Technology** including sensory augmentation, human-machine interface, wearable computers, prosthetic devices, and technology for the elderly and the disabled.
- Associate Professor in **Computer System Design**, including VLSI design, FPGA design, VLSI CAD, system-on-a-chip design, and related areas.

The campus is especially interested in candidates who can contribute to the diversity and excellence of the academic community through research, teaching, and service. Submit CV and other details by Jan. 2, 2006. For a fuller description, please see the descriptions in **this month's GRID.pdf**. Electronic applications are preferred: www.soc.ucsc.edu/jobs/faculty/apply

RFIC-2006 Call for Papers

IEEE Radio Frequency Integrated Circuits Symposium

- Paper Summary Deadline: **January 2, 2006**
- Location & Dates: San Francisco, June 11-13, 2006

Papers are solicited in these areas:

- Cellular System ICs and Architectures (GSM, EDGE, TDMA, CDMA, 3G, WCDMA, GPS)
- Wireless Data System ICs and Architectures (LAN, Bluetooth, 802.1x, Telemetry, RFID)
- Wide Band Communication System ICs and Architectures (UWB, MMDS, CATV, TV Tuners)
- Optical System ICs and Architectures (OC-48, OC-192, OC-768, Gigabit Transceivers)
- Small-Signal Circuits (LNAs, Mixers, VGAs, Active Filters, Modulators)
- Large-Signal Circuits (Power Amplifiers, Drivers, Advanced TX circuits)
- Frequency Generation Circuits (VCOs, PLLs, Synthesizers)
- RFIC & Device Technology (IC Technologies, Packaging, Modules, RF Test and Characterization)
- RFIC Modeling and CAD (Device and behavioral modeling, Design Methodology)

For the latest information, including complete Call for Papers, download the **one-page GRID announcement** or visit www.rfic2006.org

31st International

Symposium for Testing and Failure Analysis (ISTFA) [\[more\]](#)

-- San Jose Convention Center
-- Workshops: Sunday-Monday, November 6-7
-- Sessions: Tuesday - Thursday, November 8-10

If you work on dies or packages, in a wafer fabrication or board assembly facility, there's something for you at ISTFA 2005. The conference combines tutorials, an exhibition, and user group meetings, plus technical sessions with a limited number of selected, high-quality papers in two tracks.

Internationalization and Unicode Conference [\[more\]](#)

-- March 6-8, 2006
-- Hyatt Regency Hotel, Burlingame (SF Airport)
-- Tutorials: Monday, March 6
-- Sessions: Tuesday-Wednesday, March 7-8

Unicode experts, implementers, clients and vendors are invited to attend this unique conference on the Unicode Standard and internationalization. Exchange ideas with leading experts, find out about the needs of potential clients, or get information about new and existing Unicode-enabled products.

The ISTFA Tutorial (Sunday-Monday) provides coverage of critical FA topics such as Fault Isolation, MEMS, Sample Preparation, Device and Test, Failure Mechanisms and much more. Included this year is a new "Failure Analysis Basics" session with presentations on the FA process and classic case histories -- learn more about this critical field and the tools it uses.

See the one-page flyer in this month's [GRID.pdf](#), or visit www.istfa.org/.

Need consulting or outsourcing services?

Check out the [GRID Marketplace](#).

Conference topics: - Web internationalization - Text and data mining - Security and phishing - Enterprise software in a global environment - Language tags and locales: implications for developers - Global development best practices - Internationalized Domain Names/Resource Identifiers - Tips, tricks and traps in developing international software - Globalizing your product: business cases and technical issues

The Unicode Consortium is a non-profit organization founded to develop, extend and promote use of the Unicode Standard and related globalization standards.

Early-bird registration rates through January 23 - multi-attendee discounts. For more information and to register: www.unicodeconference.org/ieee

NANOWorld 2005

Nanotechnology in Aerospace and Electronics ... [\[more\]](#)

-- November 4-5, 2005 in Los Angeles

-- Loyola Marymount Univ (near LAX) -- Free Parking

NANOWorld presents the vision and directions needed to move nanotechnology out of the laboratory and into the design process. Government funding, venture capital investment and university research linked to industry are growing world-wide.

Whether you are involved with design, manufacturing or supply chain management, you WILL be affected by this revolutionary technology. Here is an opportunity to interact with the world's leading authorities on nanotechnology and meet some of the companies that are already launching nanotechnology products and services.

-- \$175 for IEEE Members, \$225 for non-Members, \$110 for students.

Program and on-line registration on the [NANOWorld website](#)

Carbon Nanotubes: Enhancing Conductivity of Conductive Plastics [\[more\]](#)

Local Chapter meeting November 9, in Sunnyvale

SCV Nanotechnology Council

Nanotech: Imagine the Opportunities

Half-Day Seminar showcasing University Graduate Research [\[more\]](#)

- Tuesday, November 15, 2005

- Noon - 5:30 PM

- Low Cost: \$35 for IEEE members, \$55 for non-members

- National Semiconductor Building 31, 955 Kifer Road, Santa Clara

See the frontier in Nanotechnology and help shape it. Novel features of this Symposium include:

- Pre-publication briefs of university graduate research topics in nanotechnology
- Challenge to the attendees to contribute imagined potential applications and opportunities that may transcend the present scope of the presented work
- Ample opportunity for networking with the graduates and other attendees

Graduate research topics to be presented include:

- Nanoscale optical devices on DNA scaffolds
- Nanotube nanofluidic transistors and circuits
- Anti-cancer drug delivery using viruses
- Ferromagnetic nanowires and high density storage
- Nanowire-based photonics and sensing
- Fabrication of nano-devices through biological catalysts

Get [more information](#), or use [the Nano Council's PayPal system](#) (for credit cards)

Upcoming classes from SVTI:

One- and Two-Day Short Courses ... [\[more\]](#)

Innovation & Creativity for High Tech Professionals (Nov. 3-4)

- how today's high-tech professional can be more creative than ever -- to survive, to stand out, and to succeed

Power Management for ICs with Focus on Linear Drop Out (LDO) Regulators (Nov. 11)

- to isolate and filter noisy supplies from increasingly sensitive mixed-signal circuits, protecting phase-locked loops and the like against switching supplies

The Dynamic Leader's Toolbox & Applications For Management in the 21st Century (Dec. 1-2)

- motivating and dealing with negative employees, assertiveness, coaching, and creative problem solving

All classes 9 AM - 4 PM at Silicon Valley Technical Institute's classrooms at 1762 Technology Dr Suite 227 (near the SJ airport) in San Jose. Find out more about each course, and register, at www.svtii.com/SVTI-calendar.htm.

Professional Skills Courses in the SF Bay Area

Choose from an array of one-day skills-development classes, from Effective Training Associates and the Engineering Management and CPMT Chapters. Classes in October and November:

Finance for Non-Finance Professionals (half-day) [\[more\]](#)

- November 1 at Cypress Semiconductor, San Jose

This course provides non-finance professionals with an understanding of basic financial terms and concepts to enable them to connect their activities and work products with the company's bottom line.

Clear Business, Technical, and E-mail Writing [\[more\]](#)

- November 3 at National Semiconductor, Santa Clara

Learn by doing - writing, revising, and editing exercises; critiquing documents; games; and lecture.

Transitioning from Individual Contributor to Manager [\[more\]](#)

- November 8 at Carl Zeiss Meditec, Dublin

Develop a deeper appreciation of the impact of one's style and greater flexibility in order to be able to deal with a variety of people.

Presentation Skills for Engineers [\[more\]](#)

- November 18 at Cypress Semiconductor, San Jose

Deliver your ideas clearly, demonstrate confidence and enthusiasm, and handle objections with poise.

Getting Things Done Across Organizational Borders [\[more\]](#)

- November 30 at Cypress Semiconductor, San Jose

Innovative practices for dealing with people who do not report to you but whose assistance and support are critical.

Other Courses: See www.effectivevtraining.com

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Technologists who are not members of IEEE are **encouraged to subscribe, at no cost, to this bi-monthly e-GRID Newsletter**, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails. This is a controlled list (ie, no spam) -- [get more information](#). You provide your own subscribe/unsubscribe services and can join and leave this IEEE DList when desired. To update the list, send an email message to:

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You may need to "whitelist" our email addresses in your spam filter to allow our Council news to reach you; I suggest whitelisting my email address (editor@e-grid.net), plus that of the

DLIST: e-GRID@listserv.ieee.org.

Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See <http://www.e-grid.net/>

Correspondence to [Paul Wesling](#), Council Communications Director
