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San Francisco Bay Area Council www.e-grid.net

Networking Opportunities at local Chapter Meetings, Tutorials, Workshops, PACE, Consultants Network Conferences, Career Enhancement

May 30, 2004

Serving the San Francisco, Oakland-East Bay, and Santa Clara Valley Sections of the IEEE
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The June issue of the **GRID.pdf** is full of opportunities to learn about new developments, master a new skill, and network with fellow professionals. We suggest that you [download the PDF file](#) and put it on your computer desktop, so you can refer to it during the month.

Check for updated information during the month at our GRID website: www.e-grid.net/

Council/Section/Chapter Meetings and Events for June

Your Career -- Your Future! Attend an evening Chapter meeting, to network with other professionals. The meetings/events below are listed in order by date. (You may use "Ctrl-F" to search for a Chapter abbreviation or keyword.)

SCV-LEOS - 6/1: **"Quantum Cryptography Using Autocompensating Fiber-Optic Interferometers"** - IBM work on quantum cryptography systems and on low-noise single-photon detectors ... [\[more\]](#)

SCV-EDS - 6/8: **"Reliability of Low-k Dielectrics: The Importance of the Barrier Interface"** - reliability is determined by the barrier/dielectric interface - Post Etch Burn Out (PEBO) is an enabling sealing concept ... [\[more\]](#)

SCV-CPMT - 6/9: **"High Density Interconnect on Flexible Substrates"** - the latest developments in high density flexible substrates for smaller form-factor packaging ... [\[more\]](#)

SCV-Comm - 6/9: **"Cyberspace Security Issues and Challenges"** - the Internet poses a unique set of security issues due to its openness and ubiquity ... [\[more\]](#)

SF-Comm - 6/10: **"Cyberspace Security Issues and Challenges"** - the Internet poses a unique set of security issues due to its openness and ubiquity ... [\[more\]](#)

SCV-PES/IAS - 6/11: **Summer Banquet** - Sumptuous dining high above Silicon Valley ... [\[more\]](#)

SCV-SP - 6/14: **"Fortran 95, or Matlab meets C++"** - surprising modern language features in Fortran 90 & 95 ... [\[more\]](#)

SCV-CNSV - 6/15: **Consultants' Network Annual Dinner: "Positive Psychology"** - Come for networking, dinner, and an entertaining speaker on psychology, success and self-help ... [\[more\]](#)

SCV-Mag - 6/15: **"It's a Small World, But I Wouldn't Want to Paint It"** - the shift to small-form-factor disk drives ... [\[more\]](#)

SCV-EMB - 6/16: **"The Artificial Synapse Chip: Towards an Electronic Prosthetic Retina"** - Progress in developing a neural interface to connect output from a digital camera to individual retinal cells ... [\[more\]](#)

SCV-SSC - 6/17: **"MicroPower Precision Floating Gate Voltage Reference"** - A new low-power voltage reference design with high accuracy is targeted at portable systems ... [\[more\]](#)

OEB-IAS - 6/17: **"Bus Protection and Current Transformer Theory"** - Current theory leads to better understanding of bus faults and bus protection methods ... [\[more\]](#)

OEB-Comm - 6/17: **"IEEE 802.11n: Multi-Antenna Techniques for High Throughput WLANs"** - focus on smart antenna architectures for 100Mbps systems ... [\[more\]](#)

SCV-CAS - 6/21: **"Signal Integrity: Design, Test and Tolerance"** - Signal integrity for nanometer technology and SoC is becoming a major challenge ... [\[more\]](#)

SCV-PSE - 6/22: **"Looking Forward, Looking Back"** - Profile of the Symposium this summer in Santa Clara and views about where we're going ... [\[more\]](#)

SCV-Rel - 6/23: **"Design and Analysis of Accelerated Reliability Tests"** - Alternatives for greatly accelerating tests and analyzing data are valuable for estimating product MTBF ... [\[more\]](#)

SCV-EMS - 6/30: **"The Dream - and Focusing to Make it Real" & "Making It - How To Go Into Business For Yourself"** - bootstrapping a new company in Silicon Valley, and making it with your own business startup ... [\[more\]](#)

Conferences and Workshops -- coming to the SF Bay Area

August 9-13: **IEEE Electromagnetic Compatibility Symposium**, Santa Clara Convention Center [\[more\]](#)

The world's EMC Experts Converge on Santa Clara this August -- five full days with workshops and special sessions presented by leaders in the industry. View numerous experiments and demonstrations, visit with over 200 exhibitors of EMC-related products and services, and learn from the experts.

Key sessions:

PCB Analysis and Design - Measurement Techniques - Shielding - Signal Integrity - Computer Modeling - Coupling - Lightning Protection - Filters and Conducted Emissions - Automotive EMC - System EMC - Modeling for Signal Integrity - Test Facilities & Instrumentation - Digital Devices - Immunity Testing - Radio Systems Interference - and more

August 16-19: **IEEE Computational Systems Bioinformatics Conference** on the Stanford University campus [\[more\]](#)

Come to CSB'04 for a broad spectrum of peer-reviewed papers covering the breadth and depth of this dynamically evolving field. Our keynote speakers, paper and poster presentations, tutorials and social events have all been designed to cater to bioinformatics' eclectic mix of disciplines. Its low registration fees make it possible for everyone to attend - it's especially easy for "locals" from the Bay Area.

What is "Bioinformatics"?

Bioinformatics - scientific and engineering disciplines bringing new biological discoveries to fields as varied as human health, agriculture, the environment, energy and biotechnology. [Find out more](#) at CSB2004.

Introductory Level Workshops Offered:

20 workshops on PCB Design (double-sided, multilayer), EMC Modeling, Signal Integrity, 802.11 Wireless - plus the NARTE EMC Engineer exams - included with full conference registration, or register for only the "workshop" day you want. (details below ...)

See the two-page flyer in this month's [GRID.pdf](#), or visit www.e-grid.net/conf/emc.html.

See the **Conference Calendar** in the [GRID.pdf](#) for additional events.

Tutorials on Monday:

Genomic Analysis - Mass Spectrometry in Proteomics - Use the Genome Browsers - Human Disease Gene Mapping - Microarray Chip Data Analysis - Protein Structure Prediction - and more

See the one-page flyer in this month's [GRID.pdf](#), or visit www.e-grid.net/conf/csb.html.

The Council Marketplace

Do you need someone with special skills -- RF, communications, design, patent consultations? ... PCB layout, thermal measurements, SCSI, SANs? See our [Council Marketplace](#) page. Thank you for supporting our advertisers.

Workshops at this year's EMC'04 in Santa Clara

(one-day registrations available for those attending only Workshops)

NARTE EMC Exam: Monday: Exam Prep Workshop - Friday: NARTE EMC Engineer Exam

PCB Design: Double-Sided and Multilayer: intended for the novice to intermediate product designer - the design steps necessary to make PCBs meet EMC requirements.

Basic EMC Measurements: an introduction to basic EMC measurements and instrumentation - EMC design methodologies that impact product performance - practical design applications.

Fundamentals of EMC Design: Understanding and applying basic design principles - how to design circuits and systems with built-in compatibility - fast rise and ultra-wide-bandwidth energy signals, sensitive sensors - medical, military, communications, IT, and instrumentation.

Introduction to Numerical EMC Modeling: all the commonly used numerical EMC modeling techniques - strengths and weakness - which are appropriate for specific problems.

IEEE 802.11 Wireless: growth and future of 802.11 - current state internationally - obstacles and regulatory requirements - relationship and convergence with other technologies.

Plus a Dozen More -- See the full details for all of them in the Advance Program. And remember - you can come for as little as one day of Workshops (minimal cost), or an afternoon at our exhibits (free) - in the heart of Silicon Valley.

SUBSCRIBE yourself to this IEEE Council and e-GRID DList

IEEE Members in the San Francisco Bay Area automatically receive this Announcement at their IEEE "email address of record." Members may unsubscribe from (or re-subscribe to) this mailing list at www.ieee.org/ra/e-notice/sfbac-enotice.html; be sure to include your IEEE member number. Members wishing to **also** subscribe an alternate email address (eg, Hotmail, or at work), or who live outside the Bay Area, may follow the instructions below.

Technologists who are not members of IEEE are **encouraged to subscribe, at no cost, to this e-GRID Newsletter**, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails. This is a controlled list (ie, no spam) -- [get more information](#). You provide your own subscribe/unsubscribe services and can join and leave this IEEE DList when desired. To update the list, send an email message to:

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You may need to "whitelist" our email addresses in your spam filter to allow our Council news to reach you; I suggest whitelisting my email address, plus that of the DLIST: e-GRID@listserv.ieee.org.

Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See <http://www.e-grid.net/>

Correspondence to [Paul Wesling](#), Council Communications Director