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Download the [October GRID.pdf](#) (600 kB) -- details on upcoming Chapter meetings, classes, conferences on a wide variety of topics. Select one of the Chapter events to learn about new developments and to network with fellow professionals. See our [QuickRef Calendar](#) for a more detailed listing of Chapter meetings.

Chapter Meetings for October, November:

OEB-IAS - 10/13 | **AC Control Power and Digital Protective Relays: Avoiding the Pitfalls** - issues and techniques for digital relays requiring a separate control power source ... [\[more\]](#)

SF-PES - 10/13 | **Harnessing the Waves and Tides to Generate Electricity** - siting, environmental and financial aspects, video news footage ... [\[more\]](#)

SCV-MTT - 10/13 | **The CloudSat Experimental Satellite 94 GHz High-Efficiency Radar Antenna** - shaped offset high-efficiency tri-reflector antenna ... [\[more\]](#)

SCV-CAS - 10/17 | **A Gentle Introduction to Microprocessor Testing** - basics of high volume manufacturing testing and major cost components ... [\[more\]](#)

SCV-Mag - 10/18 | **A Process View of Recording Head Technology** - feature sizes down to the 100 nm range in just a decade ... [\[more\]](#)

SF-Comm - 10/18 | **Broadband Over Power Lines (BPL) Internet Access on Electric Utility Lines** - the various system architectures and details of the certification requirements ... [\[more\]](#)

SCV-CNSV - 10/18 | **TEN - The Enterprise Network: Opportunities for Consultants to Work with Entrepreneurs** - ... [\[more\]](#)

SCV-Nano - 10/18 | **The First Five Years of the National Nanotechnology Initiative** - federal expenditures in nanotechnology have nearly tripled as multiple agencies have launched programs ... [\[more\]](#)

SCV-EMB - 10/19 | **Neural Basis of Reach Preparation and Communication Prosthetics** - cortically-controlled prosthetic systems based on how the brain prepares and helps execute movements ... [\[more\]](#)

SCV-SSC - 10/20 | **A Single-Chip Quad-Band GSM/GPRS Transceiver in 0.18um Standard CMOS** - fully integrated, with high sensitivity and low phase error ... [\[more\]](#)

OEB-Comm - 10/20 | **Cable Technologies: DOCSIS, PacketCable, and CableHome** - combined data, voice and video service with broad interoperability ... [\[more\]](#)

SCV-CNSV - 10/22 | **One-Day Consultants' Business Seminar** - low-cost educational seminar about Engineering Consulting as a career ... [\[more\]](#)

SF-IAS - 10/25 | **Fire Alarm Systems in CA and SF** - occupancy code requirements, system wiring and components, installation ... [\[more\]](#)

SCV-CE - 10/25 | **MPEG-21 Technology** - AVC (MPEG-4 Part 10 or H.264) video compression standard developed by joint effort ... [\[more\]](#)

OEB-PES - 10/25 | **Nikola Tesla: The Genius Who Lit the World** - 150th anniversary of his birth; AC power transmission and fundamentals of radio ... [\[more\]](#)

SCV-CPMT - 10/27 | **Drop Testing of Components in Portable Applications** - portable devices (cell phones, PDAs) are more likely to be dropped than affected by changes in thermal condition ... [\[more\]](#)

SCV-LEOS - 11/1 | **to be determined** - Steve Johnson, Lawrence Berkeley Laboratories ... [\[more\]](#)

SCV-IM - 11/2 | **Blackfin: A Combined RISC/Signal Processing Architecture for a New Era** - embedded processor for audio, video, and communications applications ... [\[more\]](#)

SCV-Mag - 11/3 | **Toggle MRAM Performance and Outlook** - magnetoresistive random access memory integrated with silicon-based microelectronics ... [\[more\]](#)

SCV-CPMT - 11/9 | **Carbon Nanotubes: Enhancing Conductivity of Conductive Plastics** - ... [\[more\]](#)

SCV-EMB - 11/16 | **High Frequency Ultrasound Imaging of the Anterior Segment of the Eye** - in-vivo intra-operative visualization of the Schlemm's canal for diagnosis and surgical guidance ... [\[more\]](#)

OEB-IAS - 11/17 | **Arc Flash Studies using Electrical Engineering Software** - a focus on how to perform an arc flash study using electrical engineering software ... [\[more\]](#)

Note to All IEEE Members

The Council Marketplace

Do you require someone with special skills -- DSP design, RF, reliability, test design, project planning, chip design, communications, Windows drivers, patent consultations? ... Reference designs, PCB layout, thermal measurements, VHDL, ANSYS, Verilog? See our [Council Marketplace](#) page. Thank you for supporting our advertisers.

Upcoming classes from SVTI:

One- and Two-Day Short Courses ... [\[more\]](#)

- Oct. 25: **Carbon NanoTubes (CNTs): Technology and Applications** these have quickly become one of the most popular materials in nanoscience and nanotechnology, with many potential applications

- Oct. 27-28: **Compact CMOS Modeling and TCAD Methodology for Nano-Scale Circuit Simulation** higher device density, faster circuit speed, and lower power dissipation, with many new physical phenomena

- Nov. 3-4: **Innovation & Creativity for High Tech Professionals** how today's high-tech professional can be more creative than ever -- to survive, to stand out, and to succeed

- Nov. 11: **Power Management for ICs with Focus on Linear Drop Out (LDO) Regulators** to isolate and filter noisy supplies from increasingly sensitive mixed-signal circuits, protecting phase-locked loops and the like against switching supplies

- Dec. 1-2: **The Dynamic Leader's Toolbox & Applications For Management in the 21st Century** motivating and dealing with negative employees, assertiveness, coaching, and creative problem solving

All classes 9 AM - 4 PM at Silicon Valley Technical Institute's classrooms at 1762 Technology Dr Suite 227 (near the SJ airport) in San Jose. Find out more about each course, and register, at www.svtii.com/SVTI-calendar.htm.

Need consulting or outsourcing services?

Check out the [GRID Marketplace](#).

RFIC-2006 Call for Papers

IEEE Radio Frequency Integrated Circuits Symposium

- Paper Summary Deadline: **January 2, 2006**

- Location & Dates: San Francisco, June 11-13, 2006

Papers are solicited in these areas:

- Cellular System ICs and Architectures (GSM, EDGE, TDMA, CDMA, 3G, WCDMA, GPS)
- Wireless Data System ICs and Architectures (LAN, Bluetooth, 802.1x, Telemetry, RFID)
- Wide Band Communication System ICs and Architectures (UWB, MMDS, CATV, TV Tuners)
- Optical System ICs and Architectures (OC-48, OC-192, OC-768, Gigabit Transceivers)
- Small-Signal Circuits (LNAs, Mixers, VGAs, Active Filters, Modulators)
- Large-Signal Circuits (Power Amplifiers, Drivers, Advanced TX circuits)
- Frequency Generation Circuits (VCOs, PLLs, Synthesizers)
- RFIC & Device Technology (IC Technologies, Packaging, Modules, RF Test and Characterization)
- RFIC Modeling and CAD (Device and behavioral modeling, Design Methodology)

For the latest information, including complete Call for Papers, visit www.rfic2006.org

FPF'05 Fall Processor Forum:

The Road to Multicore [\[more\]](#)

-- San Jose DoubleTree Hotel

-- Full-day Seminars: Mon and Thurs, October 24 & 27

-- Keynotes and Sessions: Tues - Wed, October 25-26

Integrating two or more processor cores on a single chip is the hottest trend in high-performance microprocessor design. And it's not just for PCs and servers -- embedded processors are actually setting the pace, with some new designs boasting more than a hundred cores. **FPF 2005** focuses on multicore processors, including new product introductions and other dimensions of multicore processing.

October 24-27 -- Santa Clara Convention Center

GSPx: Pervasive Signal Processing Conference and Expo

[\[more\]](#)

GSPx is dedicated to Embedded Signal Processing as the most significant and pervasive enabler in real-world applications. Decision-making engineers, managers, and executives come together at GSPx to spark ideas, develop new partnerships and make design decisions informed by what they see and learn in sessions and on the tradeshow floor. GSPx has three days of technical papers plus and a full day of technical workshops, in parallel with exhibits.

Learn about: - The Cinderella Effect of embedded memory on microprocessors - Coexistence between asynchronous and synchronous logic on SoCs - A 24-processor-core Java chip for your server rack - Programming multicore processors - IBM, Freescale, and Microsoft share ideas - The unique triple-core microprocessor in the Xbox360

See the one-page flyer in this month's [GRID.pdf](#), or visit www.in-stat.com/fpf/05.

31st International

Symposium for Testing and Failure Analysis (ISTFA) [\[more\]](#)

-- San Jose Convention Center
-- Workshops: Sunday-Monday, November 6-7
-- Sessions: Tuesday - Thursday, November 8-10

If you work on dies or packages, in a wafer fabrication or board assembly facility, there's something for you at ISTFA 2005. The conference combines tutorials, an exhibition, and user group meetings, plus technical sessions with a limited number of selected, high-quality papers in two tracks.

The ISTFA Tutorial (Sunday-Monday) provides coverage of critical FA topics such as Fault Isolation, MEMS, Sample Preparation, Device and Test, Failure Mechanisms and much more. Included this year is a new "Failure Analysis Basics" session with presentations on the FA process and classic case histories -- learn more about this critical field and the tools it uses.

See the one-page flyer in this month's [GRID.pdf](#), or visit www.istfa.org/.

NANOWorld 2005

Nanotechnology in Aerospace and Electronics ... [\[more\]](#)

-- November 4-5, 2005
-- Loyola Marymount Univ (near LAX)
-- Los Angeles
-- Free Parking

NANOWorld presents the vision and directions needed to move nanotechnology out of the laboratory and into the design process. Government funding, venture capital investment and university research linked to industry are growing world-wide. It is not a matter of "if" nanotechnology will impact the aerospace and electronics marketplaces. It is simply a matter of "when". Whether you are involved with design, manufacturing or supply chain management, you WILL be affected by this revolutionary technology. Here is an opportunity to interact with the world's leading authorities on nanotechnology and meet some of the companies that are already launching nanotechnology products and services.

Early-bird Rates through October 21!

-- \$150 for IEEE Members, \$200 for non-Members, \$100 for students
Program and on-line registration on the [NANOWorld website](#)

Application areas: - Aerospace - Audio - Automotive - Biomedical - Consumer Electronics - Digital Filtering - Digital Radio - FPGA-based DSP - Image Processing - Industrial Controls - Multi-Core Processors - Power Management - Synthetic Aperture Radar - Speech Processing & Recognition - Telephony, WiFi & VoIP ... plus others

Keynote Speakers: Dr. Andrew Viterbi (co-founder of QUALCOMM); Dave Mothersole (CTO, Freescale Semi); Dr. Rajiv Laroia (CTO, QUALCOMM's OFDMA division)

Technical Workshops

- "Preparing Software for Multi-Core Processors"
- "Model-Based Design for Video on Embedded DSPs"
- "Introduction to Multi-Standard, Multi-Core System Design"
- "DSP-FPGA Systems using C-based Design & Model-Based Design"
- "Rapid Prototyping with FPGAs"
- "Streamlining waveform portability across FPGA architectures"
- "Introduction to Video Compression"

GRID readers get an additional \$300 discount! Complimentary admission to exhibits

Visit www.e-grid.net/conf/gsp.html for more details and the **GRID** Promotion Codes to enable your discount.

IEEE Consultants' Network of Silicon Valley

Consultants' Business Seminar [\[more\]](#)

- Saturday, October 22, 2005
- 8:30 AM - 4:30 PM
- Low Cost, High Value: only \$57 for Members
- Includes Continental Breakfast and Sandwich-Buffer Lunch
- at Keypoint Credit Union, 2805 Bowers Ave, Santa Clara

CBS-2005 is an educational seminar about Engineering Consulting as a career. This event has important information for both new and experienced consultants, covering the basics in one morning track and more advanced topics in a second track.

Basics: Use Of Contracting Agencies; Internet Marketing; Rate Surveys

Advanced: Creating Your Banner Year; Whether to Incorporate; Contracts and Negotiating

Combined Track: What IEEE and AICN do for you; Key Business Trends; Outsourcing, Offshoring, & Adapting

Get [more information](#), or use [CNSV's PayPal system](#) (for credit cards)

Professional Skills Courses in the SF Bay Area

Choose from an array of one-day skills-development classes, from Effective Training Associates and the Engineering Management and CPMT Chapters. Classes in October and November:

Leadership Skills for Engineers [\[more\]](#)

- October 19 at LSI Logic, Milpitas

Today's manager must be fully aware of his/her style and be able to adjust to fit the needs of others and the demands of the situation, obtain relevant information and share it appropriately, and listen and speak for results. Learn analytical tools and cultivate "street smarts".

Communication & Conflict Management Using MBTI [\[more\]](#)

- October 20 at Cypress Semiconductor, San Jose

The Myers-Briggs Type Inventory (MBTI) is the most widely used instrument for gaining a deeper understanding of self, others and interpersonal relationships, with insights on the four basic "people patterns" that hold the key to leadership styles, effective communication, conflict, team building and productivity.

Memory Power (half-day) [\[more\]](#)

- October 27 at Exar Corporation, Fremont

You'll learn how to easily remember names, numbers, events, faces, and facts; this course is packed with practical ideas that you can use every day of your life.

Finance for Non-Finance Professionals (half-day) [\[more\]](#)

- November 1 at Cypress Semiconductor, San Jose

This course provides non-finance professionals with an understanding of basic financial terms and concepts to enable them to connect their activities and work products with the company's bottom line.

Transitioning from Individual Contributor to Manager [\[more\]](#)

- November 8 at Carl Zeiss Meditec, Dublin

Presentation Skills for Engineers [\[more\]](#)

- November 18 at Cypress Semiconductor, San Jose

Other Courses: See www.effectivevtraining.com

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Technologists who are not members of IEEE are **encouraged to subscribe, at no cost, to this bi-monthly e-GRID Newsletter**, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails. This is a controlled list (ie, no spam) -- [get more information](#). You provide your own subscribe/unsubscribe services and can join and leave this IEEE DList when desired. To update the list, send an email message to:

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You may need to "whitelist" our email addresses in your spam filter to allow our Council news to reach you; I suggest whitelisting my email address (editor@e-grid.net), plus that of the

DLIST: e-GRID@listserv.ieee.org.

Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See <http://www.e-grid.net/>

Correspondence to [Paul Wesling](#), Council Communications Director
