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August 29, 2005

Serving the San Francisco, Oakland-East Bay, and Santa Clara Valley Sections of the IEEE

For an online version of this Council announcement, with active links, visit www.e-grid.net/announcements/grid0509a.html

below: [Call for Papers: Compact Models, ISQED](#)

[Embedded Signal Processing, Semiconductor Mfgng Confs.](#)

[Antennas/Wireless and IPv6 Confs.](#)

[Local and web-delivered Courses in Sept](#)

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Download the **new September GRID.pdf** (1.0 MB) -- details on upcoming Chapter meetings, classes, conferences on a wide variety of topics. Stay ahead of changing professional demands; sharpen your competitive edge with up-to-date technical skills and through your networking activities See our [QuickRef Calendar](#) for a more detailed listing of Chapter meetings.

Chapter Meetings for September:

SCV-LEOS - 9/6 | **Silicon Photonics: Opportunity, Applications & Recent Results** - low cost opto-electronic solutions for applications from telecommunications to chip-to-chip interconnects ... [\[more\]](#)

SCV-IM - 9/7 | **Introduction to the Stanford Nanofabrication Facility, with Research Examples** - an NSF Center with a variety of semiconductor processing equipment open to external use ... [\[more\]](#)

SCV-MTT - 9/8 | **Load Pull Measurements for Device Characterization** - challenges, interactions, isolation, and calibration ... [\[more\]](#)

SCV-CPMT - 9/14 | **Kirkendall Voids in Lead-Free Solder Joints: A Reliability Issue** - effects on the impact and shock strength of solder joints ... [\[more\]](#)

OEB-IAS - 9/15 | **Power System and Equipment Grounding and Personnel Protection** - plus grounding and bonding of down conductors for lightning protection ... [\[more\]](#)

SCV-CNSV - 9/20 | **Silicon Valley: The BioTransformation of the Valley and Opportunities for Engineers** - an overview of the local biomedical industry and the newly emerging Biomedical Wave ... [\[more\]](#)

SCV-Mag - 9/20 | **Recording Performance of Discrete Track Media** - patterned magnetic disk utilizing nano-imprint lithography ... [\[more\]](#)

SF-Comm - 9/20 | **Technology and Policy Roundtable** - SBC Addresses IPTV, Fiber, Triple Play ... [\[more\]](#)

SCV-EMB - 9/21 | **Computer-aided Detection in Diagnostic Imaging** - An overview for medical imaging and diagnosis using mammography as a case study ... [\[more\]](#)

SCV-ED+SSC - 9/21 | **An Overview of MOSFET Device Behavior and Modeling for Mixed-signal/RF IC Design** - device characteristics and behavior at high frequency ... [\[more\]](#)

SF-GOLD - 9/21 | **Happy Hour With Billiards @ Jillian's** - 'Grads of Last Decade' celebrate in downtown SF (all invited) ... [\[more\]](#)

SCV-CPMT - 9/22 | **Robert Noyce: The Man Behind the Microchip** - author of the new biography on Noyce discusses his impact ... [\[more\]](#)

SF-IAS - 9/27 | **Overview of IEEE Standard 1015-1997 ("Blue Book")** - Applying Low-Voltage Circuit Breakers Used in Industrial and Commercial Power Systems ... [\[more\]](#)

SCV-Rel - 9/28 | **Built-In Soft Error Resilience for Robust System Design** - challenges of radiation-induced logic soft errors in flip-flops and combinational logic ... [\[more\]](#)

OEB-IAS - 10/1 | **Full-day Seminar: 2004 California Electrical Code** - tailored to the interest of Electrical Facility Engineers and Electrical Design Consultants ... [\[more\]](#)

SF-PES - 10/4 | **Annual Banquet: The Future of California Energy** - re-examining reliability standards and the adequacy and security of system infrastructure ... [\[more\]](#)

SCV-CPMT - 10/12 | **Imprint Patterning: An Alternative Circuit Fabrication Process** - a microreplication operation ... [\[more\]](#)

OEB-IAS - 10/13 | **AC Control Power and Digital Protective Relays: Avoiding the Pitfalls** - issues and techniques for digital relays requiring a separate control power source ... [\[more\]](#)

SCV-CNSV - 10/22 | **One-Day Consultants' Business Seminar** - low-cost educational seminar about Engineering Consulting as a career ... [\[more\]](#)

SCV-CPMT - 10/27 | **Drop Testing of Components in Portable Applications** - portable devices (cell phones, PDAs) are more likely to be dropped than affected by changes in thermal condition ... [\[more\]](#)

SCV-IM - 11/2 | **Analog Devices 'Blackfin'** - embedded processor for audio, video, and communications applications ... [\[more\]](#)

SCV-CPMT - 11/9 | **Carbon Nanotubes: Enhancing Conductivity of Conductive Plastics** - ... [\[more\]](#)

Note to All IEEE Members

The Council Marketplace

Do you require someone with special skills -- DSP design, RF, reliability, test design, project planning, chip design, communications, Windows drivers, patent consultations? ... Reference designs, PCB layout, thermal measurements, VHDL, ANSYS, Verilog? See our [Council Marketplace](#) page. Thank you for supporting our advertisers.

Your Choice of an IEEE Section

Each IEEE Member is assigned to a Section based on the mailing address (usually your home address). But this might not be the Section with which you prefer to be associated. For example, if you live in Pleasanton (and have been assigned to the Oakland-East Bay Section), but you work in San Francisco and have your professional contacts in San Francisco, then you can elect to associate yourself with the SF Section.

If you would like to revise your "home" Section, please [download and return this form](#). Once this change is made, you will receive meeting notices, newsletters and other information from the new Section, and only that Section. You will be on that new Section's roster with the mailing address you have provided.

Call for Papers for Transactions on Electron Devices:

Advanced Compact Models and 45-nm Modeling Challenges

A special issue is being devoted to research and development activities on emerging compact models for advanced circuit simulation for 65-nm silicon technology and below. Topics:
- Emerging Compact MOS Models - RF and Noise Modeling - Modeling NQS Effects - Emerging Interconnect Models - Compact Models for Emerging Devices - Model Validation - Statistical Modeling - Mixed-mode Simulation

Please see the [Call for Papers](#) for full details. More information from [Dr. Samar K. Saha](#), Silicon Storage Technology.
Deadline: January 31, 2006

CALL FOR PAPERS

7th International Symposium on

QUALITY ELECTRONIC DESIGN (ISQED 2006)

- March 27-29, 2006 (papers due Oct. 26th)

- San Jose

ISQED is the pioneer and leading international conference dealing with the design for manufacturability and quality issues front-to-back. Held next spring in San Jose, this conference is easily accessible to Bay Area authors; we invite you to submit a manuscript plus an abstract for consideration.

Summary of topics: - Design for Manufacturability & Quality - Device/Package Interactions & Co-Design - Design Verification - Design for Testability - Robust Devices, Interconnects, & Circuits - EDA Methodologies, Tools, & IP Cores - Interoperability & Reuse - Physical Design, Methodologies & Tools - Effects of Technology on IC Design, Performance, Reliability, & Yield - System-level Design, Methodologies & Tools

For full information, including web-based submission, see the [ISQED Call for Papers](#). For assistance, contact [Tanay Karnik](#), Intel

October 24-27 -- Santa Clara Convention Center

GSPx: Pervasive Signal Processing Conference and Expo [\[more\]](#)

GSPx is dedicated to Embedded Signal Processing as the most significant and pervasive enabler in real-world applications. Decision-making engineers, managers, and executives come together at GSPx to spark ideas, develop new partnerships and make design decisions informed by what they see and learn in sessions and on the tradeshow floor. GSPx has three days of technical papers plus a full day of technical workshops, in parallel with exhibits.

Application areas: - Aerospace - Audio - Automotive - Biomedical - Consumer Electronics -

ISSM'05 IEEE International Symposium on

Semiconductor Manufacturing [\[more\]](#)

-- San Jose Fairmont Hotel

-- Workshops: Monday, September 12

-- Sessions: Tuesday - Thursday, September 13-15

The Fourteenth ISSM is the largest world-wide forum specifically designed for semiconductor device manufacturers and suppliers. This year's event covers timely and important topics:

Digital Filtering - Digital Radio - FPGA-based DSP - Image Processing - Industrial Controls - Multi-Core Processors - Power Management - Synthetic Aperture Radar - Speech Processing & Recognition - Telephony, WiFi & VoIP ... plus others
Keynote Speakers: Dr. Andrew Viterbi (co-founder of QUALCOMM); Dave Mothersole (CTO, Freescale Semi); Dr. Rajiv Laroia (CTO, QUALCOMM's OFDMA division)

Technical Workshops

- "Preparing Software for Multi-Core Processors"
- "Model-Based Design for Video Systems on Embedded DSPs"
- "Introduction to Multi-Standard, Multi-Core System Design"
- "DSP-FPGA Systems using C-based Design & Model-Based Design"
- "Rapid Prototyping with FPGAs"
- "Streamlining waveform portability across FPGA architectures"
- "Introduction to Video Compression"

- Factory Design - Manufacturing Strategy and Structure - Ultra Clean Technology - Process and Metrology Equipment - Manufacturing Control and Execution - Process Materials Optimization - Environment, Safety & Health - Robust Engineering - Advanced Backend Processing - Process Control and Monitoring - Yield Enhancement

Two half-day workshops: - "Conjoint Design for Manufacturing (DFM) and Advanced Process Control (APC) Strategies for Yield at 65nm and Beyond" - "Impact on Manufacturing of Introducing New Materials in IC Production"
Six keynote speakers from Tokyo Electron, NSC, UMC, TI, Sematech, Samsung

See the one-page flyer in this month's [GRID.pdf](#), or visit www.issm.com.
Sponsors: IEEE Electron Devices and CPMT Societies, with SEMI and JSAP

GRID readers get an additional \$300 discount!

Complimentary admission to exhibits

Visit www.e-grid.net/conf/gspix.html for more details and the GRID Promotion Codes to enable your discount.

North American IPv6 Conference [\[more\]](#)

- Sept. 19-22
- San Jose State University
- Tutorials - Sessions - Exhibits - Panels

The change from Internet Protocol version 4 to IPv6 is a major upgrade in capability for the internet. Come to four days of IPv6 technology knowledge transfer to assist you with the planning and systems analysis for IPv6 deployment. IPv6 networking and IPv6 subject matter experts will discuss the architectural principles, technology advantages, mobility, transition, security, porting of applications, and other topics in depth.

One day of IPv6 tutorials allows participants to learn more about the internals of the IPv6 next-generation networks Internet protocol. The summit will also provide industry expert panels to discuss various aspects of IPv6 deployment, and vendor product panels.

Held at the SJSU Student Union Bldg (park at South Garage on 7th Street, third floor and above, \$8/day). To register, and for more information, visit www.ipv6conference.com.

The first 200 registered attendees will receive a free 12-month subscription to any of IGI's [monthly telecom newsletters](#).

Antenna Systems and Short Range Wireless '05 [\[more\]](#)

- September 22-23
- Santa Clara Marriott

More than 40 technical presentations from the foremost experts on antenna systems and short-range wireless standards & technology. One registration fee provides access to both technical programs. Focus on the most important advancements in antenna systems and short-range wireless standards and technology for industrial, commercial and residential applications.

Keynote Talks: - Small Size Active Controllable Antennas for Mobile Phones - Subscriber Based Smart Antenna For Wireless Devices: From Concept to System Integration And Beyond - RFID Technology: Promises and Challenges - Integration of Antenna on IC Package: An Elegant Antenna Solution for Single-Chip Radios

For more information and registration details, see the [Antenna Systems 2005 website](#).

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Check out the [GRID Marketplace](#).

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M.S. in Electrical Engineering (MSEE) [\[more\]](#)

Earning an M.S. degree online in Electrical Engineering will prepare you for an advanced level career as a practicing electrical engineer. Graduates of the MSEE program represent a well-respected group of highly motivated, practicing engineers. Many move on to conduct advanced research or pursue their PhDs.

M.S. in Telecommunication Networks (MSTN) [\[more\]](#)

Earning an M.S. degree online in Telecommunication Networks provides you the background that will cover all aspects of networking -- Internet banking, reservation systems, office information systems, and corporate networks.

Telecommunication Network Management Graduate Certificate [\[more\]](#)

A Graduate Certificate in Telecommunication Network Management may be pursued en route to the MSTN or MSEE - or simply to gain a specialization in this complex field of network management.

Wireless Communications Graduate Certificate [\[more\]](#)

Earning a graduate certificate in Wireless Communications will enable you to advance your career or begin an entirely new one. With a background in EE, the online courses in this program will provide you with specific knowledge required to work in this expanding market. These courses can count towards earning a Master's degree in Electrical Engineering.

Professional Skills Courses in the SF Bay Area

Choose from an array of one-day skills-development classes, from Effective Training Associates and the Engineering Management and CPMT Chapters. Classes in September and October:

Creative Problem Solving [\[more\]](#)

- September 14 at LSI Logic, Milpitas

Whatever your specific goal -- your own job or business or the systems, processes or products of your company -- you can improve the quality and quantity of your new ideas. Re-energizing your right brain for creative and breakthrough thinking could be profitable personally and professionally.

Managing Time & Multiple Priorities (half-day) [\[more\]](#)

- September 15 at Exar Corporation, Fremont

The way we elect to spend our time determines our success. This is a powerful course packed with time-saving ideas that you can use every day of your life.

Getting Things Done Across Organizational Borders [\[more\]](#)

- September 20 at BEA Systems, San Jose

Clear Business, Technical, and E-mail Writing [\[more\]](#)

- October 6 at Cypress Semiconductor, San Jose

Leadership Skills for Engineers [\[more\]](#)

- October 19 at LSI Logic, Milpitas

Memory Power (half-day) [\[more\]](#)

- October 27 at Exar Corporation, Fremont

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Technologists who are not members of IEEE are **encouraged to subscribe, at no cost, to this bi-monthly e-GRID Newsletter**, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails. This is a controlled list (ie, no spam) -- [get more information](#). You provide your own subscribe/unsubscribe services and can join and leave this IEEE DList when desired. To update the list, send an email message to:

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You may need to "whitelist" our email addresses in your spam filter to allow our Council news to reach you; I suggest whitelisting my email address (editor@e-grid.net), plus that of the DLIST: e-GRID@listserv.ieee.org.

Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See <http://www.e-grid.net/>

Correspondence to [Paul Wesling](#), Council Communications Director

