Download the February GRID.pdf (525 kB) -- details on upcoming Chapter meetings on a wide variety of topics, and for many conferences and classes. Select one of the Chapter meetings to learn about new developments and to network with fellow professionals -- choose a course or conference to master new skills. Check for updated information during the month at our GRID website: www.e-grid.net.

Chapter Meetings for February:

**SCV-Mag** - 2/15 | Spin Torque, and Nanorings - description of two new topics in magnetic nanostructures from inception to realization to potential applications ... [more]

**SCV-NANO** - 2/15 | Nanoimprint Lithography and Applications at Hewlett Packard Labs - overview of high-resolution, low cost and high throughput methods for molecular memory, logic circuits, biological sensors ... [more]

**SCV-EMB** - 2/16 | On-demand Technology Development for Medical Imaging Clinical Trials - typical challenges commonly encountered in developing technology for managing imaging-based clinical trials on a global scale ... [more]

**SCV-SSC** - 2/17 | SiGe Heterojunction Bipolar Technology and Applications - HBTs can be easily integrated into a standard CMOS flow for full-fledged manufacturing ... [more]

**OEB-IAS** - 2/17 | Short Circuit and Coordination Analysis Studies - helpful guidelines and advice on producing and then analyzing the computed and graphical results ... [more]

**OEB-COM** - 2/17 | Broadband Electromagnetic Interference Shielding for New Communication Products - methods and products solve these problems while at the same time complying with new mandated regulations ... [more]

**SCV-CNSV** - 2/22 | High Tech Recovery! Is it going to be as good as it has been? - new data on the Silicon Valley, state and US high-tech industry, for self-employed and independent consultants ... [more]

**SCV-PACE** - 2/22 | The Realities and Myths of Engineer Retraining - industry and education panel with views relevant to working engineers ... [more]

**SCV-PSES** - 2/22 | Effective Environmental Stress Screening, and Lab Tour - how to effectively apply ESS to your products, and tour of Quanta Labs ... [more]

**SCV-CE** - 2/22 | Panel Discussion on Audio-Video (AV) Networking - a clarification of which AV Networking standards to use in the Digital Home ... [more]

**SCV-Rel** - 2/23 | Best of RAMS - panel discussion of selected papers from the 2005 Reliability and Maintainability Symposium in January ... [more]

**SCV-CPMT** - 2/24 | High Frequency Fullwave Simulation of Packages ... [more]

**SCV-MBS** - 2/26 | Radiostatic Public Service Event for Demonstrating Science and Technology for the Public Welfare - free event includes many 1-hour talks, plus demonstrations of radio modes, and a vendor Bazaar ... [more]

Chapter Meetings for March:

**SCV-CAS** - 3/3 | Sigma-Delta A/D Converter Design ... [more]

**SCV-Comm** - 3/3 | Wireless Facts and Fiction - sorting out WiFi, WiMax, Mesh Networks, Smart Antennas, OFDM, UWB, CDMA ... [more]

**SCV-MTT** - 3/10 | Millimeter Wave Technology and Applications - generating, measuring, calibrating and verifying vector and scalar signals at millimeter wave frequencies ... [more]

**SCV-CPMT** - 3/16 | New, Ultrahigh Thermal Conductivity Materials - improved thermal performance and reliability, simplified thermal design, elimination of fans and heat pipes, weight savings ... [more]

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E-GRID - Your Networking Partner

Networking Opportunities at local Chapter Meetings, Tutorials, Workshops, PACE, Consultants Network Conferences, Career Enhancement

Please Post and Circulate within your Company/Lab

San Francisco Bay Area Council  www.e-grid.net

February 11, 2005

For an online version of this Council announcement, with active links, visit www.e-grid.net/announcements/grid0502b.html

<table>
<thead>
<tr>
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April 12-14, Santa Clara Convention Center
Theme: D2M -- from Design to Manufacture

WESCON Previews

Design Integration Showcase
IEEE Wescoc and the Silicon Integration Initiative (Si²) will have a special pavilion showcasing EDA IC design-for-manufacturability technology within the Design Automation Theater at the Convention Center.

Training/Learning Opportunities (all at the Santa Clara Convention Center):
Wednesday, April 13, 8:00 AM - Noon
Designs for the High-Speed, Broadband Information Age
(see more, at right)
Wednesday, April 13, 1:00 - 5:00 PM
Packaging and Test Technology for RFID
Three Tutorials on control systems technology: Wednesday, April 13, 8:30 - 11:00 AM
Motor Control Fundamentals
Wednesday, April 13, 11:30 AM - 2:00 PM
Understanding Robotic System Control
Wednesday, April 13, 2:30 - 5:00 PM
Control System Design: A Refresher

More details in the February 26th e-GRID, and on the Wesccon website!

Microwave Theory & Techniques Chapter, SCV
Designs for the High-Speed, Broadband Information Age
- 8:00 AM - Noon, Wednesday April 13, 2005
- In conjunction with IEEE WESCON/05

Four Presentations: 100GHz CMOS Circuits and the High Speed Broadband Information Age (Dr. Luiz Franca-Neto, Intel) - SiGe and RFCMOS Technology for the High Speed Information Age (Dr. Ben Yuan, IBM) - InP HBT Design for 100 to 200GHz Ics (Dr. Zachary Griffiths, UC Santa Barbara) - SiC and GaN Based Transistor and Circuit Advances (Simon Wood, Cree Microwave)

Register by April 9th to get discount rate ($50 for Members)
Download the Advance Program and registration form

New Senior Members

We salute the following SF Bay Area IEEE Members who were elevated to Senior Member during January. Each is shown with the technical affiliation.
Burnell G. West (CAS, CS, ED, SSC) - Amon Amir (UHFCC) - Rajit Chandra (CAS) - Antonio Nucci - Ronald W. Dennison (Mag) - Minoru Etoh (Comm, CS, SP) - Joseph Fjeldstad (CPMT, SIT) - Neil G. Jacobson (CS) - Mark G. Johnson (ED, SSC) - Francis Rotella (ED, MTT, SSC) - Shou-Jong Sheih - Anil Ubale (SP) - Dooseok Kim (Comm) - Vahid Madani (PI)

Congratulations!

2005 Materials Research Society Spring Meeting & Exhibit
- 28 March - 1 April 2005

IEEE/CPMT/EMS Short Courses
Professional Skills Development and Management

April 12-14, Santa Clara Convention Center
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More details in the February 26th e-GRID, and on the Wesccon website!
The MRS Exhibit features international exhibitors from all sectors of the materials science and engineering communities. Visit the exhibit to learn more about the latest techniques and advances in the swiftly evolving world of materials research directly from the manufacturers, suppliers and developers. Admission to the exhibit only is free of charge.

35 Symposia tracks with technical sessions highlight advances in the synthesis, characterization, simulation, and application of materials, from understanding materials fundamentals to fabrication of devices using electronic and electro-optic materials, nanostructured and molecular materials, and biological and hybrid materials. There are 12 Tutorials (no extra charge for attendees).

Register by March 11th to get the $100 discount.

Review the Advance Program and registration form.

Be an exhibitor at the MRS Spring Meeting - contact Mary Kaufold

Take BART or Muni to the Powell Street Station

IEEE Systems, Man, and Cybernetics Society
International Symposium on Systems and Human Science - Safety, Security, Reliability (SSR’05) [more]
- March 9-11, 2005
- Milbrae (SF International Airport Westin Hotel)

"Engineered Complex Systems in the 21st Century"
For Symposium and registration information, please access the SSR2005 website and also the Registration and Tour Application Form

The Largest System Design Event in North America
EMBEDDED SYSTEMS CONFERENCE [more]
- March 6 - 10, 2005
- Moscone Convention Center, SF

The Embedded Systems Conference offers an exciting line-up of special events. All are no-cost to registered attendees and range from keynote addresses and networking opportunities, to receptions and panel discussions, all designed to enrich your event experience.

Sunday and Monday full-day tutorials: - User Interface Design - Embedded Linux Jumpstart - Embedded C Programming - Real-Time UML - Real-Time Kernels - Managing Embedded Projects - Embedded GNU Jumpstart - TCP/IP Networking and more, plus more; and 132 three-hour and 90-minute Technical Classes on Tuesday through Friday

Flexible Registration Packages: - 1-day, 2-day, 3-day, full 5-day, or the ePass value

- No-cost Exhibits Pass (includes Keynotes and Panels)

Register by March 3rd for discount rates.

Ride BART or MUNI to the Powell Street station

"Design for Quality in the Era of Uncertainty"
International Symposium on Quality Electronic Design [more]
- March 14th - 18th, 2005
- Double Tree Hotel, San Jose

ISQED is the pioneer and leading conference dealing with CAD and design for manufacturability and quality issues, front to back.

TUTORIALS (all day Monday): - Design of sub-90nm CMOS Circuits and Design Methodologies - Package-Chip Co-Design


Register by March 14th for discount rates.

Get complete information

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Technologists who are not members of IEEE are encouraged to subscribe, at no cost, to this e-GRID Newsletter, to find opportunities to "network" with the electrical engineering community and hear about Chapter events through our twice-monthly emails. This is a controlled list (ie, no spam) - get more information. You provide your own subscribe/unsubscribe services and can join and leave this IEEE DLlist when desired. To update the list, send an email message to:

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Or, to receive a text-only notification email, use the following in the BODY (not Subject) of the message:

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unsubscribe e-GRID-text

Maintain your current DLlist records at our IEEE LISTSERV website: http://listserv.ieee.org/

You may need to "whitelist" our email addresses in your spam filter to allow our Council news to reach you; I suggest whitelisting my email address, plus that of the DLLIST: e-GRID@listserv.ieee.org.

Please invite other non-Member technologists in the Bay Area to join this list, to receive notification of Chapter meetings, tutorials, workshops, conferences, and other news within the San Francisco Bay Area Council, IEEE. See http://www.e-grid.net

Correspondence to Paul Wesling, Council Communications Director