DesignCon 2006 -- Connecting the World of Electronic Design

-- Conference February 6-9, 2006
-- Santa Clara Convention Center
-- Exhibits February 7-8

Developed specifically for semiconductor and electronic design engineers, DesignCon is the essential design engineering event addressing the challenges facing these communities and providing the solutions attendees can implement immediately in their designs. Find your solution at DesignCon 2006.

Technical Tracks:
- Chip-Level Functional Design
- Chip-Level Physical Design and Verification
- Power and Package Co-Design
- PCB, Package, and Passive Technologies
- Chip and Board Interconnect Design
- High-Performance Backplane Interconnect Design
- High-Speed Timing, Jitter and Noise
- Functional Verification (and more)

Management Forum Panels:
- Engineering Education in the United States
- Engineering in the United States Today's DFM: the Supply-Chain and its Effect on Designers
- The Growing Cost of EDA Tools: When to Build versus Buy
- The Business of DFM: Critical Issues and Implications (and more)

For the complete program and to register: www.designcon.com

Early-Bird rates through January 15th.
Sign up for an exhibits pass.

Need consulting or outsourcing services? Check out the GRID Marketplace.

The IEEE First International Conference on Collaborative Computing: Networking, Applications and Worksharing

-- Conference December 19-21, 2005
-- DoubleTree Hotel, San Jose
-- Sessions in two tracks

The premier international forum for discussions among academic and industrial researchers, practitioners, and students interested in collaborative networking.

Int'l Consumer Electronics Show:

The 2006 Storage Visions Conference

-- January 6 & 7, 2006
-- Las Vegas Convention Center
-- Concurrent with the Consumer Electronics Show
-- Admission to the CES Exhibits

Hear the latest on digital content technology from industry leaders -- those who matter in the entertainment value chain -- and find out how current and coming trends can benefit your company, from the experts at Turner Broadcast, Seachange, Microsoft, Scientific Atlanta, Seagate, Maxtor, GS Magic, Samsung Semiconductor, M-Systems, Agere Systems, Marvell, SV Microelectronics, HP, Atmel, Sonic Solutions, Silicon Image, Toshiba, and many others.

Sessions:
- Storage and Content Creation, Editing and Distribution
- Home Network Storage
- Integration of Storage in CE Devices
- Mobile CE Storage Products
- Optical Storage
- Storage and CE Analyst Session
- Financial Analyst and VC Session

For complete information, please see the Storage Visions 2006 website: www.storagevisions.com, or call Kathleen locally at 408-871-8808, to register for the Conference and CES Show Exhibits.

Also at the CES in Las Vegas, from EE TIMES & CMP:

Great Minds, Great Ideas: the People, the Products, and the Technologies That Will Change Our World

-- January 5, 2006 at the Sands Hotel, Las Vegas
-- Concurrent with the Consumer Electronics Show
-- no-cost event
9:00 AM: Disruptive Innovation 10:30 AM: Game Theory 1:00 PM: New Frontiers 2:30 PM: Innovation Generation 4:00 PM: Digital Media

More details on the Consumer Electronics Show website

Object Management Group:

Internationalization and Unicode Conference

-- March 6-8, 2006
-- Hyatt Regency Hotel, Burlingame (SF Airport)
-- Tutorials in three tracks: Monday, March 6
-- Sessions in three tracks: Tuesday-Wednesday, March 7-8

Unicode experts, implementers, clients and vendors are invited to attend this unique conference on the Unicode Standard and internationalization. Exchange ideas with leading experts, find out about the needs of potential clients, or get information about new and existing Unicode-enabled products.
worksharing, and applications.


For more information and to register: www.collaboratecon.org
(At-door registration is available.)

Faculty Positions: UC-Santa Cruz Computer Engineering Dept
Assistant and Associate Professor Positions

- Assistant Professor with research interests in the area of Assistive Technology including sensory augmentation, human-machine interface, wearable computers, prosthetic devices, and technology for the elderly and the disabled.

- Associate Professor in Computer System Design, including VLSI design, FPGA design, VLSI CAD, system-on-a-chip design, and related areas.

The UC-Santa Cruz campus is especially interested in candidates who can contribute to the diversity and excellence of the academic community through research, teaching, and service. Submit CV and other details by Jan. 2, 2006. For additional details, please see the descriptions in this month's GRID.pdf. Electronic applications are preferred: www.soe.ucsc.edu/jobs/faculty


Early-bird registration rates through February 1 - multi-attendee discounts. For more information and to register: www.unicodeconference.org/ieee

RFIC-2006 Call for Papers
IEEE Radio Frequency Integrated Circuits Symposium

[more]
- Paper Summary Deadline: January 2, 2006
- Location & Dates: San Francisco, June 11-13, 2006

Papers are solicited in these areas:
- Cellular System ICs and Architectures - Wireless Data System ICs and Architectures - Wide Band Communication System ICs and Architectures - Optical System ICs and Architectures - Small-Signal Circuits - Large-Signal Circuits - Frequency Generation Circuits - RFIC & Device Technology - RFIC Modeling and CAD

For the latest information, including complete Call for Papers, download the one-page GRID announcement or visit www.rfic2006.org

Call for Papers -- IEEE Computer Society
Symposium on Field-Programmable Custom Computing Machines

[more]
- April 24-26 2006
- Napa Valley Marriott
- Abstracts Due January 13

FCCM is a forum for presenting new research on reconfigurable computing -- the use of field-programmable technologies for high performance and/or low energy computation. Submissions are solicited on a wide variety of topics related to custom computing, including but not limited to:
- Architecture of reconfigurable computing devices and systems - Languages, compilation techniques, tools, and environments for programming and run-time support - Applications of reconfigurable computing in scientific computation, mobile communications, medical image processing, data and communication security, network infrastructure - Possible forms and system implications of reconfiguration for fault tolerance and avoidance - Novel use of reconfigurability, including evolvable hardware and adaptive computing

Please download the full Call for Papers and add the FCCM dates to your calendar.

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Correspondence to Paul Wesling, Council Communications Director